Test Procedure for V194 Pulse Fanout Module

This procedure just tests front panel input signals and not VMEbus P2 connector inputs. The P2 test procedure is available on the Controls Hardware Documents web page.

1. Component installation

A. Check that all components are installed correctly and soldered.
B. Check with DVM for any shorts on +5 V. Do this on capacitor C5 or C6.

2. Jumper Patch Requirements

A. E1 to E2
B. E3 to E4
C. E5 to E6
D. E7 to E8
E. On JP1 J1 input, jumper 9 to 16
F. On JP2 J2 input, jumper 10 to 15
G. On JP3 J3 input, jumper 11 to 14
H. On JP4 J4 input, jumper 12 to 13
I. JP9 terminator, no jumper
J. JP10 terminator, no jumper
K. JP11 terminator, no jumper
L. JP12 terminator, no jumper

3. Equipment Needed

A. Pulse Generator with TTL output into 50 ohms.
B. 2 ea. Cables with Lemo connectors on one end and BNC on other.
C. Oscilloscope
D. 6 U VME chassis

4. Testing

A. Set pulse generator to 1 us pulse width and 100 ms period.
B. Connect output of generator into input 1 of V194.
C. Connect V194 output 1A to oscilloscope channel 1 with it set for 50 ohm input. Output signal should look similar to input. Move cable to 1B, 1C, and 1D all should have the same signal as 1A.
D. Move generator output to input 2 of V194. Outputs 2A,2B,2C and 2D should look the same.
E. Move generator output to input 3 of V194. Outputs 3A,3B,3C and 3D should look the same.
F. Move generator output to input 4 of V194. Outputs 4A,4B,4C and 4D should look the same.
G. Test is done.